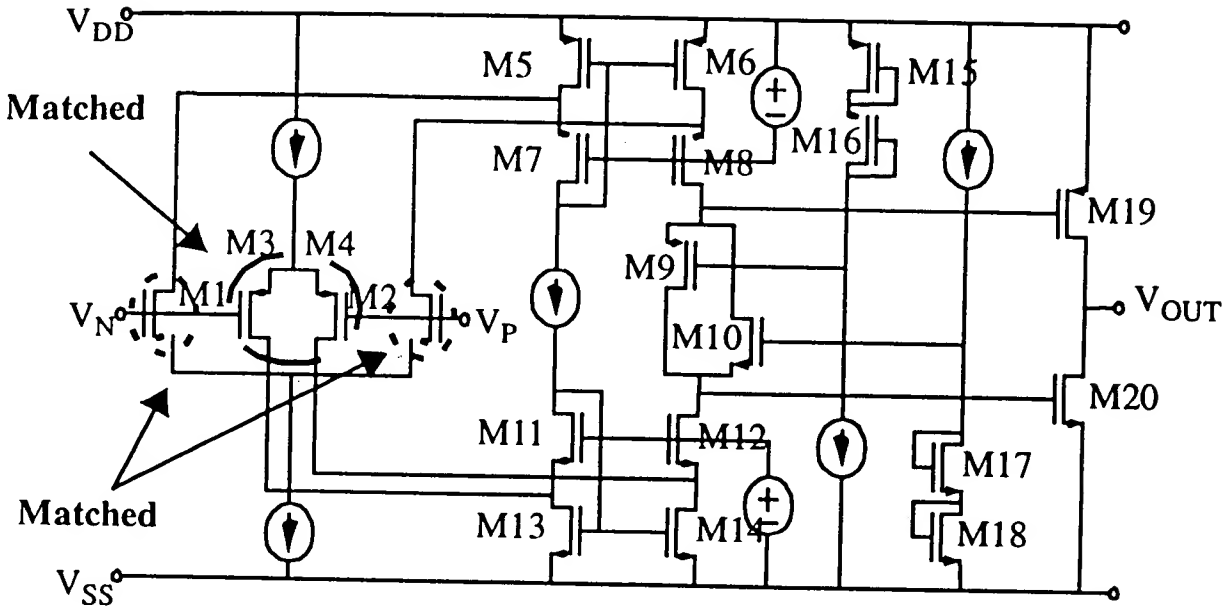
V_{DD}
Matched
M5
M6
M15
M16
M7
M8
M19
M3
M4
M9
M1
M2
V_N
V_P
V_{OUT}
M10
M20
M11
M12
M17
Matched
M13
M14
M18
V_{SS}

FIGURE 1

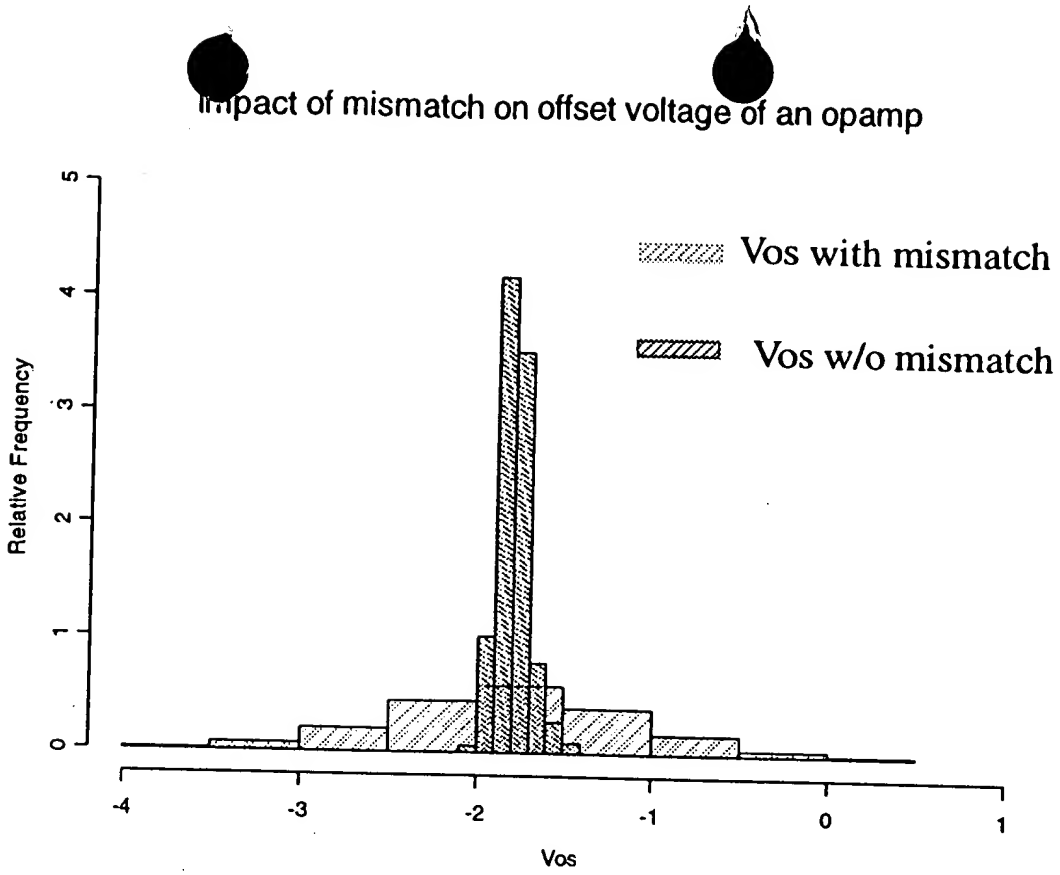

FIGURE 2

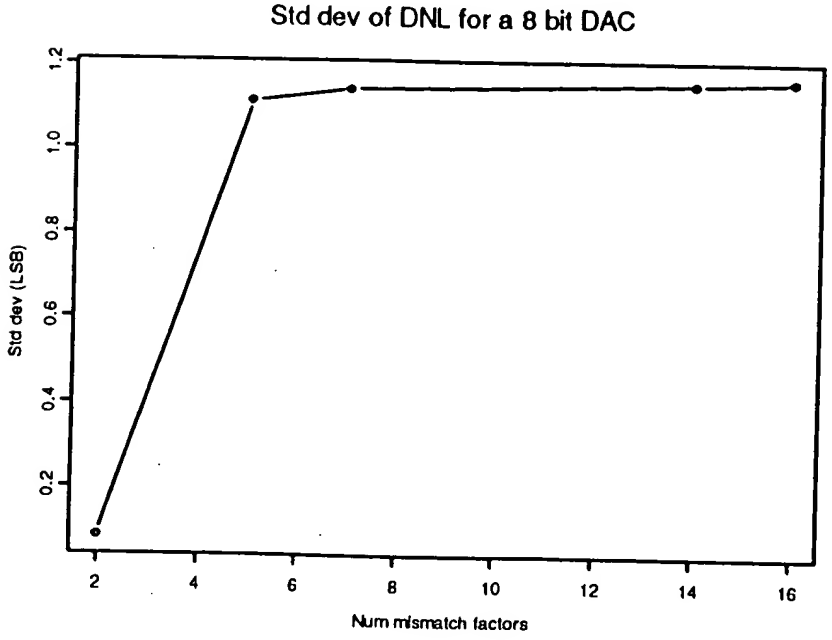

FIGURE 4

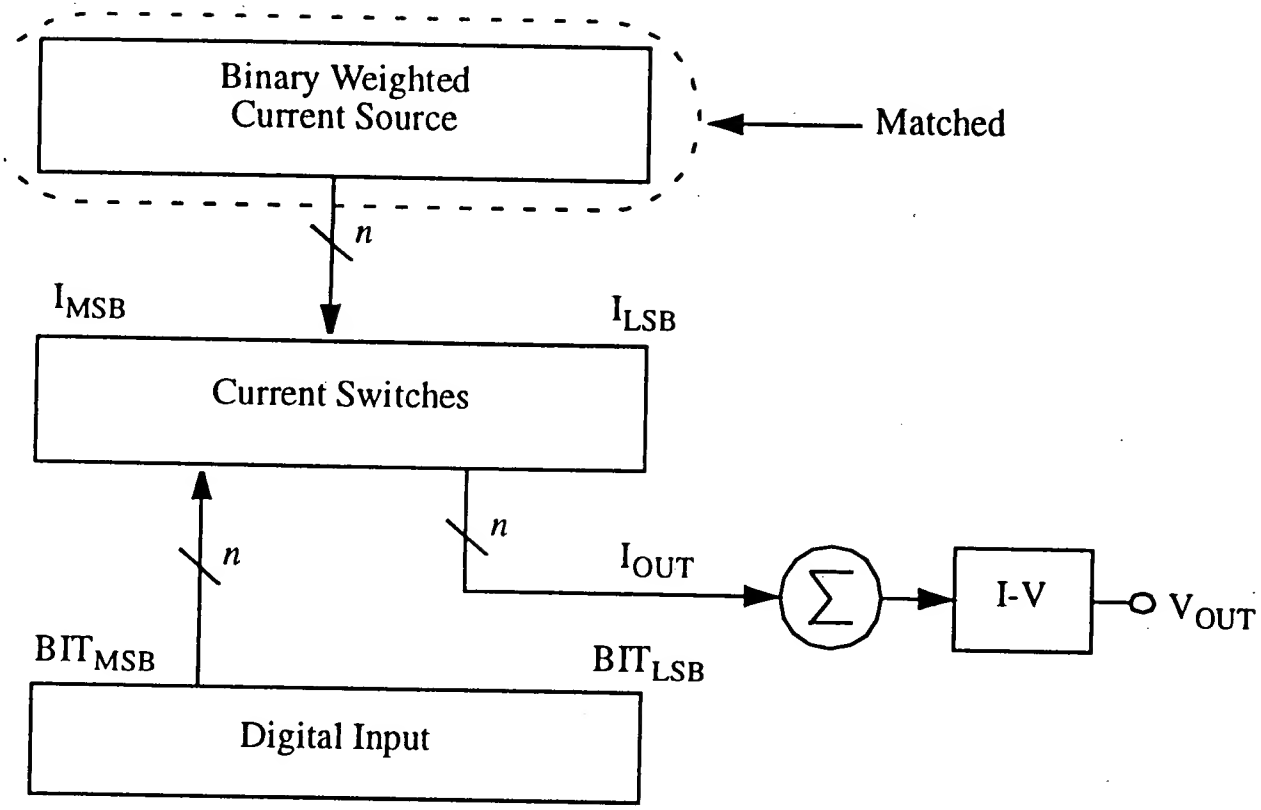
Binary Weighted
Current Source
Matched
n
I_{MSB}
I_{LSB}
Current Switches
n
n
I_{OUT}
Σ
I-V
V_{OUT}
BIT_{MSB}
BIT_{LSB}
Digital Input

FIGURE 3